### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Nguyen et al.

Attorney Docket No.: NSC1P205D2

Application No.: Not yet assigned

Examiner: Not yet assigned

Filed: August 29, 2003

Group: Not yet assigned

Title: TECHNIQUES FOR JOINING AN OPTO-

ELECTRONIC MODULE TO A SEMICONDUCTOR PACKAGE

CERTIFICATE OF EXPRESS MAILING

I hereby certify that this paper and the documents and/or fees referred to as attached therein are being deposited with the United States Postal Service on August 29, 2003 in an envelope as "Express Mail Post Office to Addressee" service under 37 CFR §1.10, Mailing Label Number EV334021507US, addressed to the Comprisorper for Patents, Washington, DC 20231.

Jeffrey Ng

INFORMATION DISCLOSURE STATEMENT 37 CFR §§1.56 AND 1.97(b)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449 may be material to examination of the above-identified patent application. Applicants submit the list of these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application. The above-identified application is a divisional of prior application U.S. Patent Application No. 09/947,210. This prior application is being relied upon for an earlier filing date under 35 U.S.C. § 120. Because the listed references were either cited by the PTO, or submitted to the PTO in the prior application, under 37 CFR § 1.98(d) Applicants submit that copies need not be provided.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is: (i) filed within three (3) months of the filing date of the above-referenced application, (ii) believed to be filed before the mailing date of a first Office Action on the merits, or (iii) believed to be filed before the mailing of a first Office Action after the filing of a Request for Continued Examination under §1.114. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure

Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. NSC1P205D2).

Respectfully submitted, BEYER WEAVER & THOMAS, LLP

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Registration No. 52,937

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Form 1449 (Modified)	Atty Docket No.	Application No.:
ļ	NSC1P205D2	Not yet assigned
Information Disclosure	Applicant:	
Statement By Applicant	Nguyen et al.	
1	Filing Date	Group
(Use Several Sheets if Necessary)	August 29, 2003	Not yet assigned

## **U.S. Patent Documents**

Examiner						Sub-	Filing
Initial	No.	Patent No.	Date	Patentee	Class	class	Date
	A	5,011,246	04/30/91	Corradetti et al.			
	В	6,305,848	10/23/01	Gregory	T		
	C	6,201,704	03/13/01	Poplawski et al.			
	D	6,318,909	11/20/01	Giboney et al.			
	E	5,487,124	01/23/96	Bowen et al.			
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	G	6,043,430	03/28/00	Chun	T		
	H	6,236,109	05/22/01	Hsuan et al.			
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	K	6,316,837	11/13/01	Song			
	L	2001/0013645	08/16/01	King et al.			
	M	6,316,838	11/13/01	Ozawa et al.			
	N	5,726,079	03/10/98	Johnson			

Foreign Patent or Published Foreign Patent Application

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Examiner		Document	Publication	Country or		Sub-	Trans	lation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
	0	JP 08-125066	05/17/96	Japan				
	P	60-202956	10/14/85	Japan				

## **Other Documents**

No.	Author, Title, Date, Place (e.g. Journal) of Publication
Q	S. Savastiouk, PH.D., et al. "3-D stacked wafer-level packaging", March
,	2000, Advanced Packaging, pgs. 28-34.
R	National Semiconductor, "Packaging Databook", 1993 National
	Semiconductor, pgs. v-xi to 1-3 to 1-4, 3-1 to 3-20, 3-30 to 3-31, 3-62 to 3-69.
	Please note: The year of publication is sufficiently earlier than the effective
	U.S. filing date so that the particular month of publication is not in issue.
	Date Considered
	Q

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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, , , ,	NSC1P205D2	Not yet assigned
Information Disclosure	Applicant:	
Statement By Applicant	Nguyen et al.	
, , , ,	Filing Date	Group
(Use Several Sheets if Necessary)	August 29, 2003	Not yet assigned

## **U.S. Patent Documents**

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	T	5,579,208	11/26/96	Honda et al.			
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	W	5,608,262	03/04/97	Degani et al.			
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	AA	2001/0019173	09-2001	Taguchi et al.			
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Foreign Patent or Published Foreign Patent Application

Examiner		Document	Publication	Country or		Sub-	Trans	slation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
							7	
							1	

# **Other Documents**

Examiner			
Initial No. Author, Title, Date, Place (e.g. Journal) of Publication			
	<del>-</del>		
Examiner		Date Considered	

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.